Curriculum Vitae

Personal Data

Given name:	Maria
Family name:	Koziri
Sex:	Female
Date of birth:	26 June 1978
Marital status:	Married
Children:	3 (2011, 12, 14)
Nationality:	Greek
Address:	45 Markou Botchari st., 35100 Lamia, Greece
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Education

2003-07: PhD, Dept. of Computer and Communications Eng., Univ. of Thessaly, Volos, Greece. Thesis title: "Algorithms and Implementation for Macroblock Prediction in H.264 Video Coding Standard". Advisors: Georgios Stamoulis and Ioannis Katsavounidis.

1997-03: Diploma, School of Computer and Electrical Eng., Technical Univ. of Crete, Greece.

Foreign Languages

English (Excellent knowledge - Cambridge Proficiency)
Italian (Medium knowledge)
French (Medium knowledge)

Awards and Distinctions

2006: Silver Leaf Certificate at the Int. Conf. on Ph.D. Research in Microelectronics and Electronics (IEEE PRIME) for the paper entitled: "A Low-Power VLSI Architecture for Intra and Inter Prediction in H.264", together with G.I. Stamoulis, and I. Katsavounidis.

Professional Experience

2017-today: Assistant Professor, Dept. of Computer Science and Telecommunications, University of Thessaly (UTH), Lamia, Greece.

2013-2017: Visiting Lecturer, Dept. of Computer Science, University of Thessaly (UTH), Lamia, Greece.

2008-11: Visiting Lecturer, Dept. of Computer and Communications Eng., Univ. of Thessaly (UTH), Volos, Greece.

2004-13: Visiting Lecturer, Dept. of Informatics and Computer Technology, Technological Educational Institute (TEI) of Lamia, Lamia, Greece.

2003-04: Instructor, 2nd Technical School, Volos, Greece.

Grants (participation)

2018-today: "ENORASI: Intelligent Audio-visual System Enhancing Cultural Experience and Accessibility", European Union and Greek national funds through the Operational Program Competitiveness, Entrepreneurship and Innovation, under the call RESEARCH-CREATE-INNOVATE (~1M€)

2014-15: "SeNSE: Audio-Visual Sensor Networks for Densely Sampling and Replaying Events".
Funding scheme: European Social Fund and National Resources - COOPERATION 2011.
Contracted by: National and Kapodistrian Univ. of Athens.
Role: System architecture design. Research and software development for video coding. System evaluation. Publicity actions.

2010: "Avid-Mode: Efficient Location-Aware Audio-Visual Delivery of High-Quality Content to Mobile Devices".
Funding scheme: FP7-Marie Curie Actions, Industry-Academia Partnerships and Pathways.
Contracted by: Cidana (Shanghai) Co. Ltd.
Role: Visiting researcher at Cidana (Shanghai) with the role of conducting a comparative study between H.264 and VP8 coding standards.

2007: "Porting OpenAVS in Tensilica's Diamond 388VDO platform and documentation creation".
Funding scheme: Tensilica Inc.
Contracted by: Tensilica Inc.

Role: Code development and optimization of the porting using Tensilica Instruction Extensions (TIEs). Code integration and testing. Documentation writing.

2007-08: "NETNET – Wireless Networks and Services".
Funding scheme: European Social Fund and National Resources.
Contracted by: Univ. of Thessaly, Greece.
Role: Organization and tutoring of seminars concerning wireless network services. Co-author of deliverables.

2006: "Distant learning in the Technological Educational Institute (TEI) of Lamia".
Funding scheme: European Social Fund and National Resources-EPEAEK II.
Contracted by: TEI of Lamia, Greece.
Role: Development of distant learning material for the course: "Computer Architecture". Teaching Instructor using Blackboard platform.

2004-05: "Calculating and reducing voltage drop in modern digital circuits".
Funding scheme: Intel Corp.
Contracted by: Univ. of Thessaly, Greece.
Role: Implementation of the theoretical model for voltage drop calculation and conduct of simulation experiments.

2004-05: "Integrated circuit design for signal processing of capacitance microsensors".
Funding scheme: European Social Fund and National Resources.
Contracted by: Univ. of Thessaly, Greece.
Role: Physical design and CAD support.

2003 (Scholarship for 2006-07):
"Distant learning in the Dept. of Comp. and Comm. Eng. of the Univ. of Thessaly".
Funding scheme: European Social Fund and National Resources-EPEAEK II.
Contracted by: Univ. of Thessaly, Greece.
Role: Research on hardware implementations of H.264.

2002: "UP-TV: Ubiquitous Personalized Interactive Multimedia TV Systems and Services".
Funding scheme: EU, IST.
Contracted by: Technical Univ. of Creete.
Role: System development for transaction support over Web.

2002: "UWA: Ubiquitous Web Applications".
Funding scheme: EU, IST.

Contracted by: Technical Univ. of Creete.
Role: Development of tools for transaction design with suitable interfaces to facilitate system integration.

Teaching Experience

Dept. of Computer Science, Univ. Thessaly, Lamia, Greece

- (PG course) Advanced Techniques for Video Compression (Ac. Years: 2018-today)
- (PG course) Multimedia systems and databases (Ac. Years: 2015-2018)
- Signals and Systems (Ac. Years: 2015- today)
- Digital Image Processing (Ac. Years: 2015- today)
- Digital Signal Processing (Ac. Years: 2015- today)
- Video Compression Techniques (Ac. Years: 2016- today)
- Computer Architecture (Ac. Year: 2015-16)
- VLSI Design (Ac. Year: 2015-16)
- Computer Organization (Ac. Years: 2014-16)
- Introduction to Computers (Ac. Years: 2013- today)
- (Lab) Logic Design (Ac. Years: 2013- today)

Dept. of Electrical & Computer Eng., Univ. Thessaly, Volos, Greece

- (PG course) Multimedia Processing (Ac. Years: 2016-today)
- (Lab) Introduction to Computers (Ac. Years: 2008-10)
- (Lab) Signals and Systems Theory (Ac. Year: 2010-11)
- (Lab) Digital Design I (Ac. Years: 2008-11)

Dept. of Informatics, Technological Educational Institute (TEI) of Central Greece, Lamia, Greece

- Computer Architecture (Ac. Years: 2005-08)
- Advanced Computer Architectures (Ac. Years: 2005-08)
- Microprocessors – Microcontrollers (Ac. Year: 2008-09)
- Design and Analysis of Algorithms (Ac. Year: 2009-10)
- Introduction to Computer Systems (Ac. Year: 2009-10)
- Selected Topics in Object Oriented Programming (Ac. Year: 2003-04)
- Virtual Network Design (Ac. Year: 2003-04)
- Lab Programming I (Ac. Year: 2009-10)
- Lab Programming II (Ac. Year: 2004-05)
- Lab Network Design (Ac. Year: 2010-11)
- Lab Databases (Ac. Year: 2011-12)
- Lab Data Structures (Ac. Years: 2011-13)

- Lab Computer Architecture (Ac. Years: 2006-08)
- Lab Advanced Computer Architectures (Ac. Years: 2004-08)
- Lab Computer Architecture I (Ac. Years: 2011-13)
- Lab Computer Architecture II (Ac. Years: 2011-13)
- Lab Design and Analysis of Algorithms (Ac. Years: 2009-10, 11-12)
- Lab Microprocessors – Microcontrollers (Ac. Year: 2008-09)
- Lab Combinatorial Digital Electronics (Ac. Year: 2008-09)

Students

PhD
- Nikolaos Kalyvas (co-advisor) UTH, 2019-today.
- Natalia Panagou (advisor) UTH, 2018-today.
- Panagiotis Papadopoulos (informal co-advisor) UTH, 2016-today.
- Panagiotis Oikonomou (informal co-advisor), UTH, 2015-today.
- Tina Kokkari (informal co-advisor), UTH, 2016-today.
- Dr. Antonios N. Dadaliaris (informal co-advisor), UTH, 2007-12. Currently works as a Visiting Lecturer in the Univ. of Thessaly.

MSc
- Tina Kokkari (informal co-advisor), UTH, 2016.
- Evangelia Nerantzaki (informal co-advisor), UTH, 2017.
- Panagiotis Papadopoulos (informal co-advisor) UTH, 2017.
- Athanasios Kosmidis (informal co-advisor), UTH, 2016-today.
- Efstathia Soufleri (informal co-advisor), UTH, 2016- today.

Social and Community Service

- Head of the non-profit organization “ΙΣΟΝ” which targeted at actions against social exclusion (2004-09).
- Member of the organizing committee of the art festival Belle Arte Lamia (2005, 06 and 07).
- Member of the Lamia Group for Raising Awareness on the Breast Feeding Benefits (2010-today).

Research Interests

Primary research interests fall in the area of video coding. In particular:

- Motion estimation and motion compensation algorithms.
- Hardware implementations of video codec.
- Parallel video coding (SIMD, slices, tiles).

- Scalable Video Coding (SVC).
- RD (Rate Distortion) optimization.
- Video transcoding with emphasis on Cloud related implementations and optimizations.

Other research interest include:
- Video transmission.
- Resource optimization in distributed systems with a focus on Cloud provisioning for video coding and delivery.
- Applications of Kalman filters.
- Hardware implementation of algorithms.
- Cell placement and optimization problems in VLSI design.

Journal Reviewing

- IEEE Transactions on Circuits and Systems for Video Technology.

Citations

Scholar google: 172.
Researchgate: 130.

Book Translations

2006: Member of the team for the Greek translation of the book: "Computer Architecture" by D. Patterson and J. Hennessy.

Publications

Book Chapters

[BC2] T. Loukopoulos, **M. Koziri**, N. Panagou, P. Papadopoulos, and D. Iakovidis, Cloud Video Guidance as "Deus ex Machina" for the Visually Impaired, in *EAI/Springer Innovations in Communications and Computing: Technological Trends in Improved Mobility of the Visually Impaired*, pp. 127-143, 2020, ISBN 978-3-030-16449-2.

[BC1] K. Kolomvatsos, **M.G. Koziri**, and T. Loukopoulos, Chapter 11: A Recommendation System for Allocating Video Resources in Multiple Partitions, *Big Data Recommender Systems: Recent Trends and Advances-Volume 2: Application Paradigms, IET, 2019*, e-ISBN: 9781785619786.

Journal Papers (peer-reviewed)

[J9] **M. G. Koziri**, P. K. Papadopoulos, N. Tziritas, T. Loukopoulos, S. U. Khan, and A. Y. Zomaya, "Efficient Cloud Provisioning for Video Transcoding: Review, Open Challenges and Future Opportunities," *IEEE Internet Computing (IC)*, vol. 22(5), pp. 46-55, Sept. 2018.

[J8] N. Tziritas, **M. Koziri**, A. Bachtsevani, T. Loukopoulos, G. Stamoulis, S. U. Khan, and C.-Z. Xu, "Data Replication and Virtual Machine Migrations to Mitigate Network Overhead in Edge Computing Systems," *IEEE Trans. Sustainable Computing (TSUSC)*, vol. 2(4), pp. 320-332, Oct.-Dec. 2017.

[J7] A. N. Dadaliaris, P. Oikonomou, **M. G. Koziri**, E. Nerantzaki, T. Loukopoulos, G. I. Stamoulis, "A Connectivity-Based Legalization Scheme for Standard Cell Placement," *Circuits and Systems (SciRP CS)*, vol. 8(8), pp. 191-201, Aug. 2017.

[J6] **M. G. Koziri**, P. K. Papadopoulos, N. Tziritas, A. N. Dadaliaris, T. Loukopoulos, G. I. Stamoulis, "On Planning the Adoption of New Video Standards in Social Media Networks: A General Framework and its Application to HEVC," *Social Network Analysis and Mining (Springer SNAM)*, vol. 7(1), pp. 1-32, July 2017.

[J5] A. N. Dadaliaris, P. Oikonomou, **M. G. Koziri**, E. Nerantzaki, Y. Hatzaras, D. Garyfallou, T. Loukopoulos, and G. I. Stamoulis, "Heuristics to Augment the Performance of Tetris Legalization: Making a Fast but Inferior Method Competitive," *J. Low Power Electronics (JOLPE)*, vol. 13(2), pp. 220-230, 2017.

[J4] P. Oikonomou, **M. G. Koziri**, N. Tziritas, T. Loukopoulos, and C.-Z. Xu, "Scheduling Heuristics for Live Video Transcoding on Cloud Edges," *ZTE Comminations*, vol.15(2), pp. 35-41, 2017.

[J3] **M.G. Koziri**, T. Loukopoulos, M. Adam, and N. Assimakis, "Speedup of Kalman and Lainiotis Filters for Partitionable Models," *Int. J. of Adv. Comp. Res. (IJACR)*, vol. 6(26), pp. 160-166, 2016.

[J2] N. Assimakis, M. Adam, **M.G. Koziri**, S. Voliotis, and K. Asimakis, "Optimal Decentralized Kalman Filter and Lainiotis Filter," *Digital Signal Processing*, *Elsevier*, vol. 23(1), pp. 442-452, 2013.

[J1] **M.G. Koziri**, D. Zacharis, I. Katsavounidis, and N. Bellas, "Implementation of the AVS Video Decoder on a Heterogeneous Dual-Core SIMD Processor," *IEEE Trans. Consumer Electronics*, vol. 57(2), pp. 673-681, 2011.

Conference papers (peer-reviewed)

[C36] N. Panagou, **M. Koziri**, P. K. Papadopoulos, P. Oikonomou, N. Tziritas, K. Kolomvatsos, T. Loukopoulos, and S. U. Khan, "Evaluation of Heterogeneous Scheduling Algorithms for Wavefront and Tile Parallelism in Video Coding," *Proc. 2019 Int. Conf. on Internet of Things (ICIOT 2019), Springer*, San Diego, USA, June 2019, pp. 16-27.

[C35] P. K. Papadopoulos, N. Panagou, **M. Koziri**, K. Kolomvatsos, T. Loukopoulos, and I. Anagnostopoulos, "Coding Time Prediction in H.264/HEVC Transcoding Using Macroblock Sizes," *Proc. 14th Int. Workshop on Semantic and Social Media Adaptation and Personalization (SMAP 2019), IEEE*, Larnaca, Cyprus, June 2019. *(to appear)*

[C34] N. Panagou, P. Oikonomou, P. K. Papadopoulos, **M. G. Koziri**, T. Loukopoulos, and D. Iakovidis, "On Predicting Bottlenecks in Wavefront Parallel Video Coding Using Deep Neural Networks," *Proc. 20th Int. Conf. on Engineering Applications of Neural Networks Workshops (EANN 2019 Workshops), Springer*, Crete, Greece, May 2019, pp. 501-510.

[C33] N. Panagou, P. K. Papadopoulos, **M. G. Koziri** and T. Loukopoulos, "On Evaluating the Impact of Tile Partitioning in AV1," *Proc. 22nd Panhellenic Conf. on Informatics (PCI 2018), ACM*, Athens, Greece, Nov. 2018, pp. 121-126.

[C32] T. Loukopoulos, N. Tziritas, **M. Koziri**, G.I. Stamoulis and S.U. Khan, "A Pareto-Efficient Algorithm for Data Stream Processing at Network Edges", *Proc. 10th Int. Conf. on Cloud Computing Technology and Science (Cloudcom 2018), IEEE*, Nicosia, Cyprus, Dec. 2018, pp. 159-162.

[C31] N. Tziritas, S. Mustafa, **M. Koziri**, T. Loukopoulos, S.U. Khan, C.-Z. Xu, and A.Y. Zomaya, "Server Consolidation in Cloud Computing", *Proc. 24th Int. Conf. on Parallel and Distributed Systems (ICPADS 2018), IEEE*, Singapore, Dec. 2018, pp. 194-203.

[C30] K. Kolomvatsos, **M. Koziri**, and T. Loukopoulos, "An Intelligent Scheme for the Identification of QoS Violations in Virtualized Environnments," *Proc. 30th Int. Conf. on Tools with Artificial Intelligence (ICTAI 2018), IEEE,* Volos, Greece, Nov. 2018, pp. 1040-1047.

[C29] K. Kolomvatsos, P. Oikonomou, **M. Koziri**, and T. Loukopoulos, "A Distributed Data Allocation Scheme for Autonomous Nodes," *Proc. 18th Int. Conf. on Scalable Computing and Communications (SCALCOM 2018), IEEE,* Guangzhou, China, Oct. 2018, pp. 1651-1658.

[C28] P.K. Papadopoulos, **M.G. Koziri**, N. Tziritas, T. Loukopoulos, I. Anagnostopoulos, Petr Šaloun, and D. Andrešić, "On the Evaluation of Coarse Grained Parallelism in AV1 Video Coding," *Proc. 13th Int. Workshop on Semantic and Social Media Adaptation and Personalization (SMAP 2018), IEEE*, Zaragoza, Spain, Sept. 2018, pp. 55-59.

[C27] P.K. Papadopoulos, **M.G. Koziri**, and T. Loukopoulos, "A Fast Heuristic for Tile Partitioning and Processor Assignment in HEVC," *Proc. ICIP 2018, IEEE,* Athens, Greece, Oct. 2018, pp. 4143-4147.

[C26] **M.G. Koziri**, P.K. Papadopoulos, and T. Loukopoulos, "Combining Tile Parallelism with Slice Partitioning in Video Coding," *Proc. SPIE 10752, Applications of Digital Image Processing XLI*, 107520N, San Diego, USA, Sept. 2018.

[C25] P. Oikonomou, **M.G. Koziri**, N. Tziritas, A.N. Dadaliaris, T. Loukopoulos, G.I. Stamoulis, and S.U. Khan, "Scheduling Video Transcoding Jobs in the Cloud," *Proc. GreenCom 2018, IEEE,* Vancouver, Canada, Aug. 2018, pp. 442-449.

[C24] T. Loukopoulos, N. Tziritas, **M. Koziri**, G. I. Stamoulis, S.U. Khan, C.-Z. Xu, and A.Y. Zomaya, "Data Stream Processing at Network Edges," *Proc. 8th IEEE Workshop on Parallel/Distributed Computing and Optimization* (*PDCO 2018-IPDPS workshops)*, *IEEE*, Vancouver, Canada, May 2018, pp. 657-665.

[C23] T. Loukopoulos, **M.G. Koziri**, K. Kolomvatsos, and P. Oikonomou, "On Green Scheduling for Desktop Grids," *Proc. 6th World Conf. on Information Systems and Technologies (WorldCIST)*, *Springer*, Naples, Italy, March 2018, pp. 330-340.

[C22] P.K. Papadopoulos, N.C. Zygouris, **M.G. Koziri**, T. Loukopoulos, and G.I. Stamoulis, "Mobivoke: A Mobile System Architecture to Support Off School Collaborative Learning Process," *Proc. 11th Int. Conf. on Interactive Mobile Communication, Technologies and Learning (IMCL 2017)*, *Springer*, Thessaloniki, Greece, Nov. 2017, pp. 587-592.

[C21] D. Skoumpourdis, P. K. Papadopoulos, **M. G. Koziri**, N. Tziritas, T. Loukopoulos, and I. Anagnostopoulos, "On Improving the Speedup of Slice and Tile Level Parallelism in HEVC Using AVX2," *Proc. 21st Panhellenic Conf. on Informatics (PCI 2017)*, *ACM*, Larisa, Greece, Sept. 2017, pp. 52:1-52:6.

[C20] **M. Koziri**, P. K. Papadopoulos, N. Tziritas, N. Giachoudis, T. Loukopoulos, S. U. Khan, and G.I. Stamoulis, "Heuristics for Tile Parallelism in HEVC," *Proc. 25th European Signal Processing Conf. (EUSIPCO 2017)*, *IEEE*, Kos, Greece, Aug. 2017, pp. 1514-1518.

[C19] P. Oikonomou, **M. G. Koziri**, A.N. Dadaliaris, T. Loukopoulos, and G.I. Stamoulis, "Domocus: Lock-Free Parallel Legalization in Standard Cell Placement," *Proc. 6th Int. Conf. on Modern Circuits and Systems Technologies (MOCAST 2017)*, *IEEE*, Thessaloniki, Greece, May 2017, pp. 1-4.

[C18] **M.G. Koziri**, T. Loukopoulos, M. Adam, N. Assimakis, and G. Tzialas, "On the Optimal Processor Assignment for Computing the Steady State Kalman Filter in Parallel and Distributed Systems," *Proc. 5th World Conf. on Information Systems and Technologies (WorldCIST)*, *Springer*, Madeira, Portugal, April. 2017, vol. 3, pp. 427-437.

[C17] **M.G. Koziri** and T. Loukopoulos, "Sensor Selection for Resource-Efficiency Query Execution in IoT Environments," *Proc. 8th Int. Conf. on Cloud Computing, GRIDs and Virtualization (Cloud Computing 2017), IARIA*, Athens, Greece, Feb. 2017, pp. 49.

[C16] **M. G. Koziri**, P. Papadopoulos, N. Tziritas, A.N. Dadaliaris, T. Loukopoulos, S.U. Khan, and C.-Z. Xu, "Adaptive Tile Parallelization for Fast Video Encoding in HEVC," *Proc. 12th Int. Conf. on Green Computing and Communications (GreenCom 2016)*, *IEEE*, Chengdu, China, Dec. 2016, pp. 738-743.

[C15] A. N. Dadaliaris, E. Nerantzaki, **M.G. Koziri**, P. Oikonomou, T. Loukopoulos, and G.I. Stamoulis, "Performance Evaluation of Tetris-based Legalization Heuristics," *Proc. 20th Panhellenic Conf. on Informatics (PCI 2016)*, *ACM*, Patras, Greece, Nov. 2016, no. 60.

[C14] P. Papadopoulos, **M. G. Koziri**, N. Tziritas, T. Loukopoulos, I. Anagnostopoulos, and G.I. Stamoulis, "Performance Evaluation of Batch Encodings in HEVC Using Slice

Level Parallelism," *Proc. 20th Panhellenic Conf. on Informatics (PCI 2016), ACM*, Patras, Greece, Nov. 2016, no. 70.

[C13] M.G. Koziri, P. Papadopoulos, N. Tziritas, A.N. Dadaliaris, T. Loukopoulos, and G.I. Stamoulis, "A Framework for Scheduling the Encoding of Multiple Smart User Videos," *Proc. 11th Int. Workshop on Semantic and Social Media Adaptation and Personalization (SMAP 2016), IEEE*, Thessaloniki, Greece, Oct. 2016, pp. 29-34.

[C12] M.G. Koziri, P. Papadopoulos, N. Tziritas, A.N. Dadaliaris, T. Loukopoulos, and S.U. Khan, "Slice-Based Parallelization in HEVC Encoding: Realizing the Potential Through Efficient Load Balancing," *Proc. 18th Int. Workshop on Multimedia Signal Processing (MMSP 2016), IEEE*, Montreal, Canada, Sept. 2016, pp. 1-6.

[C11] P. Oikonomou, **M. G. Koziri**, A.N. Dadaliaris, Y. Hatzaras, E. Neratzaki, and G.I. Stamoulis, "Heuristics for Iterative Detailed Standard Cell Placement," *Proc. South East European Design Automation, Computer Engineering, Computer Networks and Social Media Conf. (SEEDA-CECNSM 2016), ACM*, Kastoria, Greece, Sept. 2016, pp. 19-24.

[C10] P. Oikonomou, T. Loukopoulos, A.N. Dadaliaris, **M.G. Koziri**, and G.I. Stamoulis, "On Formulating and Tackling Integrated Circuit Placement as a Scheduling Problem," *Proc. 19th Panhellenic Conf. on Informatics (PCI 2015), ACM*, Athens, Greece, Oct. 2015, pp. 86-91.

[C9] M.G. Koziri and A. Eleftheriadis, "Joint Quantizer Optimization For Scalable Coding," *Proc. 17th IEEE Int. Conf. on Image Processing (ICIP 2010), IEEE*, Hong Kong, China, Sept. 2010, pp. 1281-1284.

[C8] M.G. Koziri, N. Bellas, I. Katsavounidis, and D. Zacharis, "Implementation of the AVS Video Decoder on a Heterogeneous Dual-Core SIMD Processor," *Digest of Technical Papers 2010 IEEE Int. Conf. on Consumer Electronics (ICCE 2010), IEEE*, Las Vegas, NV, USA, Jan. 2010, pp.267-268.

[C7] N. Bellas, I. Katsavounidis, **M.G. Koziri**, and D. Zacharis, "Mapping the AVS Video Decoder on a Heterogeneous Dual-Core SIMD Processor," *IEEE Design Automation Conference (DAC 2009)*, user track paper, San Francisco, CA, USA, July 2009.

[C6] N. Assimakis, M. Adam, **M.G. Koziri**, and S. Voliotis, "Optimal Distributed Kalman and Lainiotis Filters: Optimal Uniform Distribution of Measurements into Local Processors," *Proc. 16th Int. Conf. on Systems, Signals and Image Processing (IWSSIP 2009), IEEE*, Chalkida, Greece, June 2009, pp. 1-6.

[C5] M. Owaida, **M.G. Koziri**, I. Katsavounidis, and G.I. Stamoulis, "A High Performance and Low Power Hardware Architecture for the Transform and Quantization Stages in H.264," *Proc. 2009 IEEE Int. Conf. on Multimedia and Expo (ICME 2009), IEEE*, New York City, NY, USA, June 2009, pp. 1102-1105.

[C4] M.G. Koziri, A.N. Dadaliaris, G.I. Stamoulis, and I. Katsavounidis, "A Novel Low-Power Motion Estimation Design for H.264," *Proc. 18th IEEE Int. Conf. on Application-Specific Systems, Architectures and Processors (ASAP 2007), IEEE*, Montreal, Canada, July 2007, pp. 247-252.

[C3] **M.G. Koziri**, G.I. Stamoulis, and I. Katsavounidis, "Power Reduction in an H.264 Encoder Through Algorithmic and Logic Transformation," *Proc. 2006 Int. Symp. on Low Power Electronics and Design (ISLPED 2006), ACM*, Tagernsee, Germany, Oct. 2006, pp. 107-112.

[C2] **M.G. Koziri**, G.I. Stamoulis, and I. Katsavounidis, "A Low-Power VLSI Architecture for Intra and Inter Prediction in H.264", *Proc. 2nd IEEE Conf. on PhD Research in Microelectronics and Electronics (PRIME 2006), IEEE,* Otranto (Lecce), Italy, June 2006, pp. 109-112. **(*silver leaf award*)**

[C1] G.I. Stamoulis, **M.G. Koziri**, I. Katsavounidis, and N. Bellas, "A Low – Power VLSI Architecture for Intra Prediction in H.264", *Proc. 10th Panhellenic Conf. on Informatics (PCI 2005), Springer LNCS*, Volos, Greece, Nov. 2005, pp. 633-640.

Recommendation Letters

Prof. Georgios I. Stamoulis,
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